

SDS2836EF

Small Signal Fast Switching Diode

General Description

Dual general-purpose switching diodes, fabricated in planar technology, and packaged in small SOT-523F surface mounted device (SMD) packages.

Features and Benefits

- Silicon epitaxial planar diode
- High switching speed: trr≤4ns
- · Low forward drop voltage and low leakage current
- Full lead (Pb)-free and RoHS compliant device
- Available in "Green" device











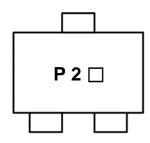
Applications

· Ultra high speed switching application

Ordering Information

Part Number	Marking Code	Package	Packaging
SDS2836EF	P2 □	SOT-523F	Tape & Reel

Marking Information



P 2 = Specific Device Code

☐ = Year & Week Code Marking

Pinning Information

Pin	Description	Simplified Outline	Graphic Symbol
1	Cathode (Diode 1)	3	
2	Cathode (Diode 2)		
3	Common Anode	1 2	

Rev. date: 25-AUG-10 KSD-D5E005-000 www.auk.co.kr

Absolute Maximum Ratings (T_{amb}=25°C, Unless otherwise specified)

Characteristic	Symbol	Ratings	Unit
Maximum repetitive peak reverse voltage	V _{RM}	85	V
Continuous reverse voltage	V _R	80	V
Maximum average forward rectified current	Io	100	mA
Forward current (DC)	I _F	100	mA
Maximum repetitive peak forward current	I _{FM}	300	mA
Non-repetitive peak forward surge current(t=10ms)	I _{FSM}	2	А
Power dissipation 1)	P _D	150	mW

¹⁾ Device mounted on FR-4 board with recommended pad layout.

Thermal Characteristics (T_{amb} =25°C, Unless otherwise specified)

Characteristic	Symbol	Ratings	Unit
Thermal resistance, junction to ambient 1)	R _{th(j-a)}	830	°C/W
Operating junction temperature	Tj	150	°C
Storage temperature range	T _{stg}	-55 ~ 150	°C

¹⁾ Device mounted on FR-4 board with recommended pad layout.

Electrical Characteristics (T_{amb}=25°C, Unless otherwise specified)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Forward voltage ²⁾	V _{F(1)}	I _F =1mA	ı	0.6	-	V
	$V_{F(2)}$	I _F =10mA	ı	0.7	-	٧
	$V_{F(3)}$	I _F =100mA		0.9	1.2	V
Reverse leakage current 3)	I _R	V _R =80V		-	0.5	uA
Total capacitance	C_T	V _R =0V, f=1 MHz		2.2	4.0	pF
Reverse recovery time	t _{rr}	I _F =10mA (Fig. 5)	-	1.6	4.0	ns

²⁾ Pulse test: t_P≤380 µs, Duty cycle≤2%

 $^{^{3)}}$ Pulse test: $t_P \le 5 \, \text{ms}$, Duty cycle $\le 2\%$

Rating and Characteristic Curves

Fig. 1) Typical Forward Characteristics

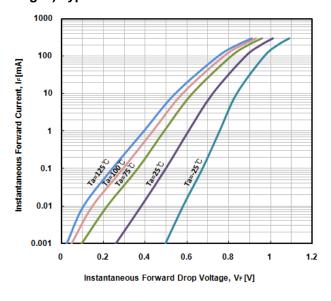


Fig. 2) Typical Reverse Characteristics

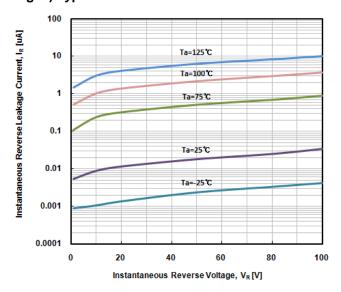


Fig. 3) Typical Total Capacitance Characteristics

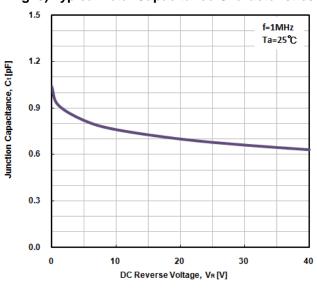


Fig. 4) Reverse Recovery Time vs. Forward Current

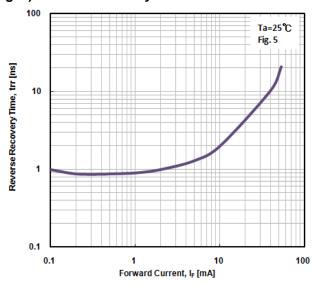
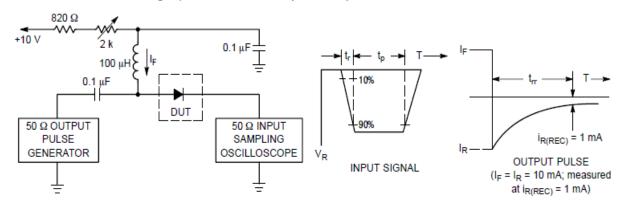
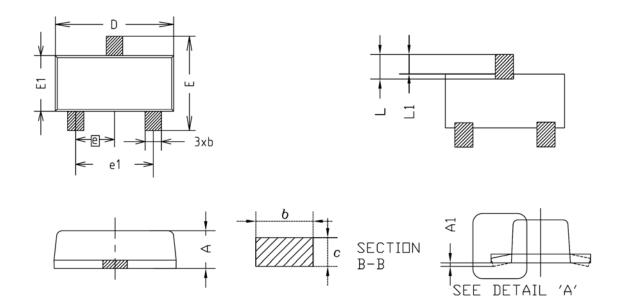


Fig. 5) Reverse recovery time equivalent test circuit

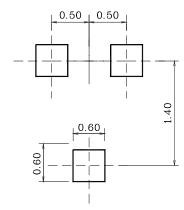


Package Outline Dimensions



SYMBOL	MILLIMETERS			NOTE
STABOL	MINIMUM	NOMINAL	MAXIMUM	NUIL
Α	0.63	0.68	0.73	
A1	0.00	ı	0.10	
A2	_	ı	_	
b	0.25	0.30	0.35	
C	0.04	0.11	0.20	
D	1.50	1.60	1.70	
Ε	1.50	1.60	1.70	
E1	0.78	0.88	0.98	
е	0.50BSC			
e1	0.90	ı	1.10	
L	0.34	0.44	0.54	
L1	0.28	0.34	0.43	

X Recommend PCB solder land (Unit : mm)



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